

Figure 1b

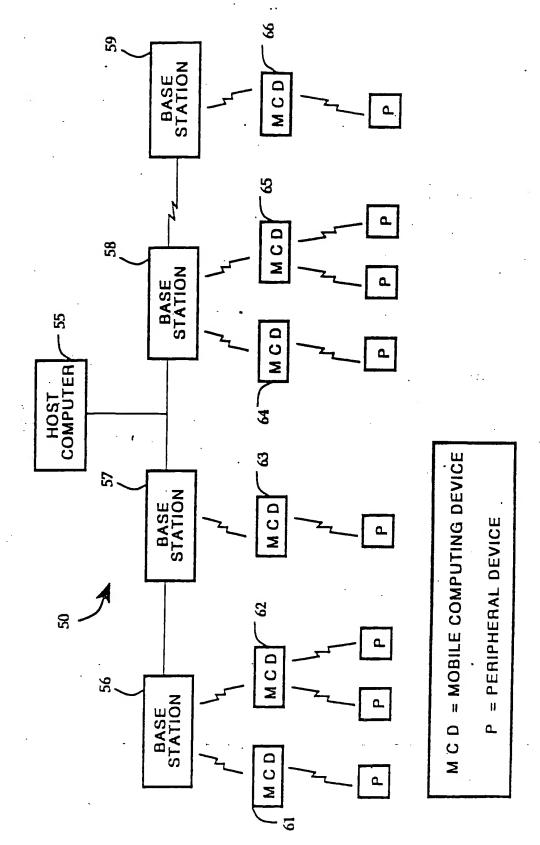


Figure 1c

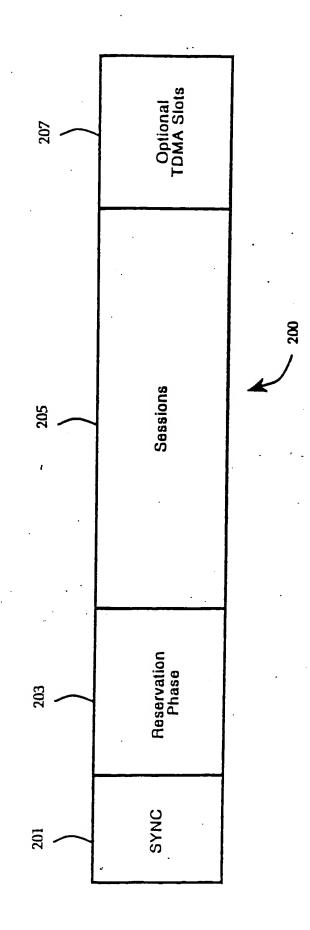


Figure 2

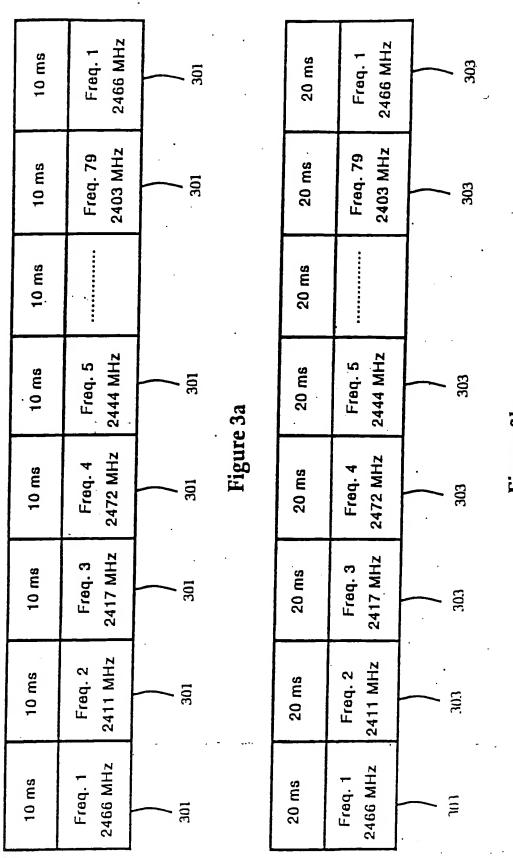


Figure 3b

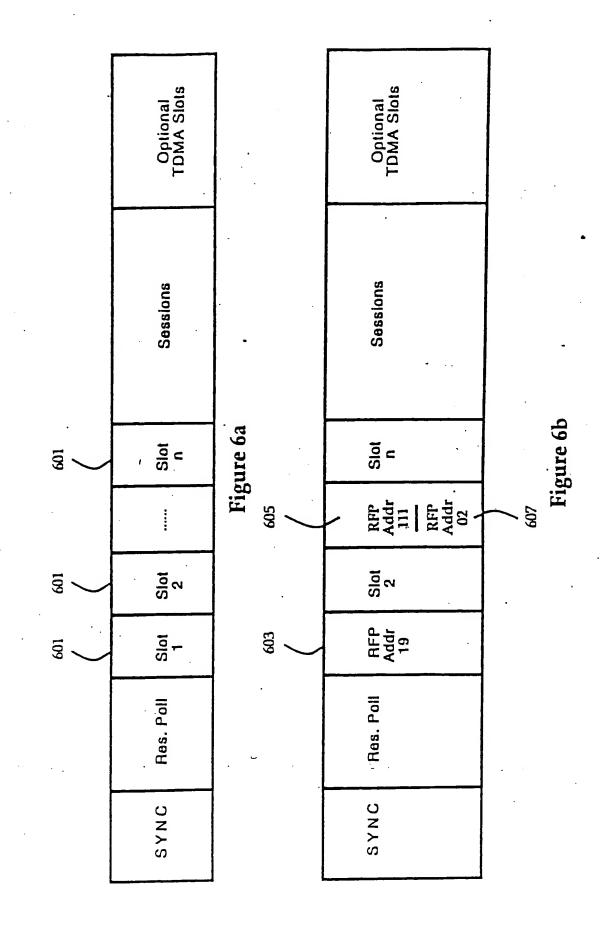
Freq 1: 2466 MHz	.466 MHz	Freq 2: 2	oq 2: 2411 MHz	Freq 3: 2417 MHz	117 MHz	Freq 5: 3	Freq 5: 2444 MHz
AI. 1	AI. 2	Al. 1	Al. 2	Al. 1	AI. 2	Al. 1	A S
20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms
*							21107
/ 401					,		

Figure 4a

		Al. 4 20 ms
	Freq 2: 2411 MHz	Al. 3 20 ms
	Freq 2:	Al. 2 20 ms
		Al. 1 20 ms
		AI. 4 20 ms
	6 MHz	Al. 3 20 ms
	Freq 1: 2466 MHz	AI. 2 20 ms
•	•	Al. 1 20 ms

Figure 4b

Figure 5b



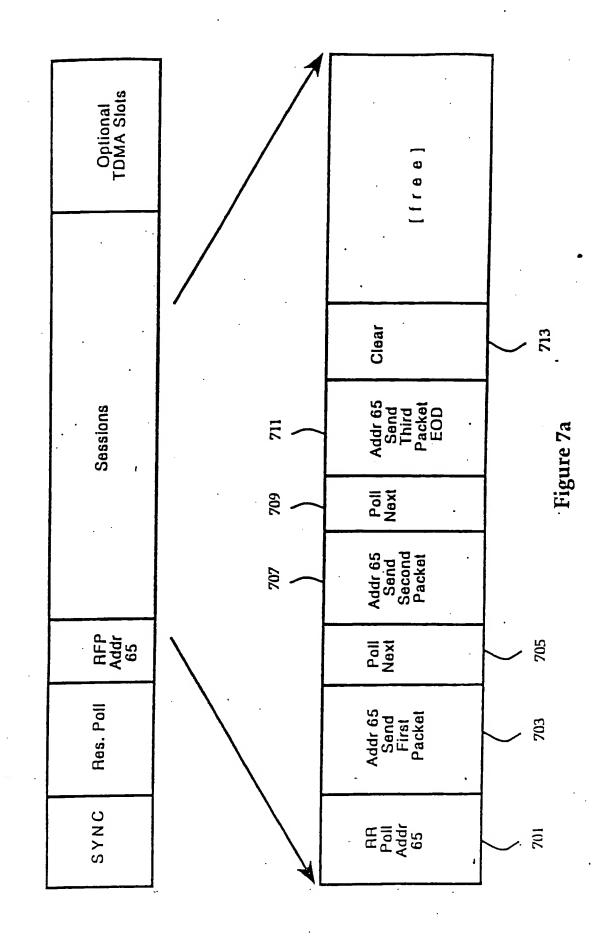
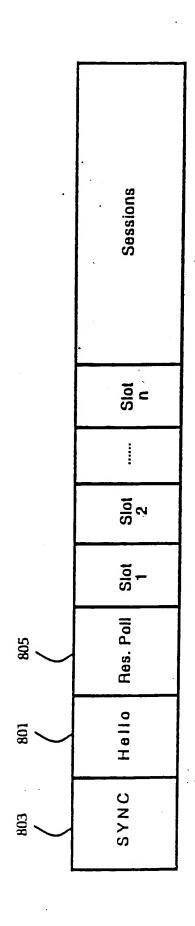
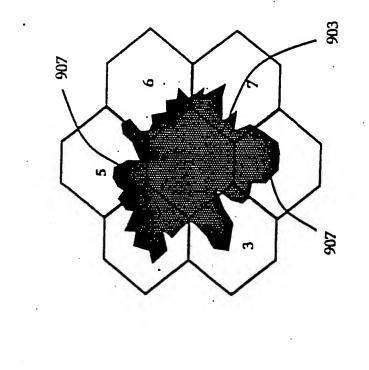


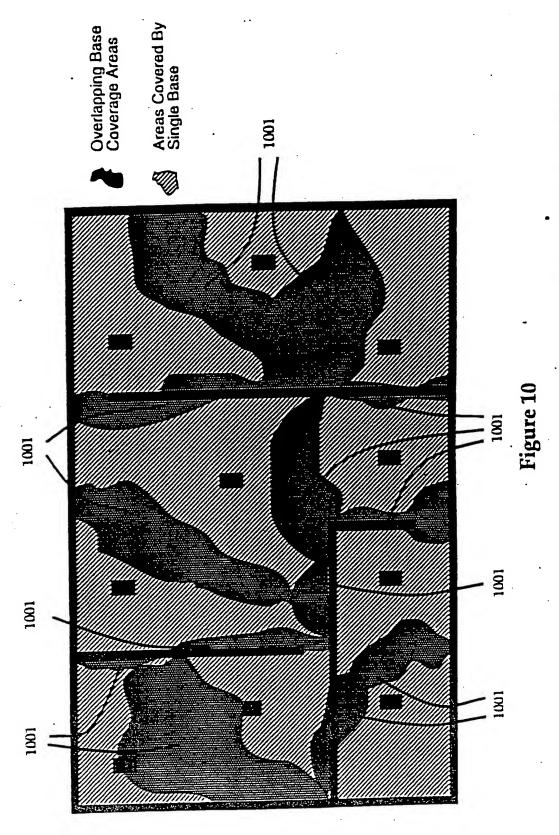
Figure 7b





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Figure 9a



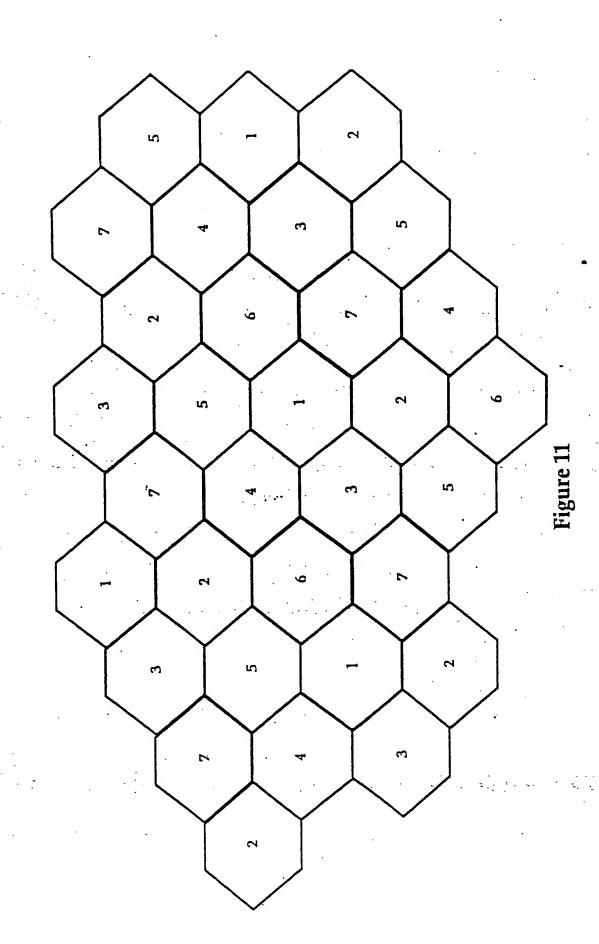
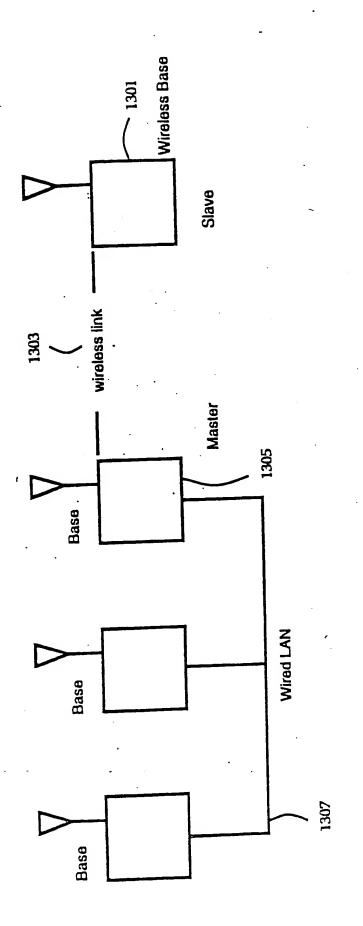
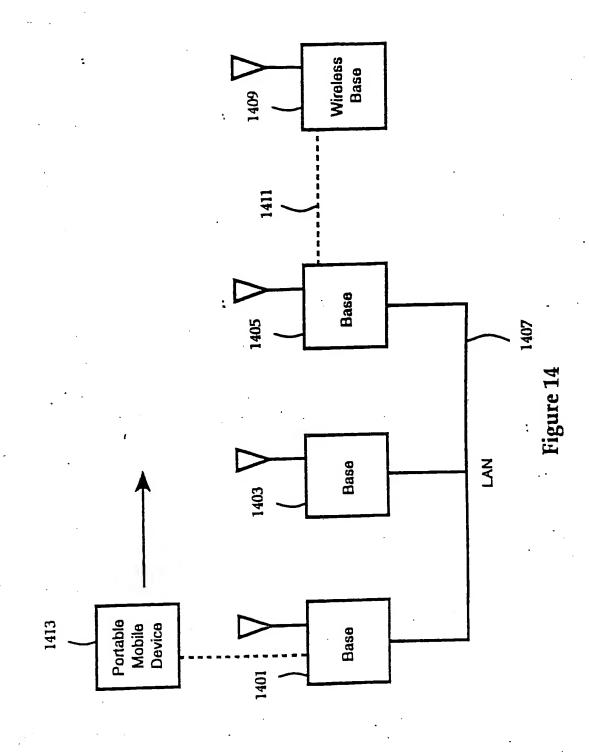


Figure 12





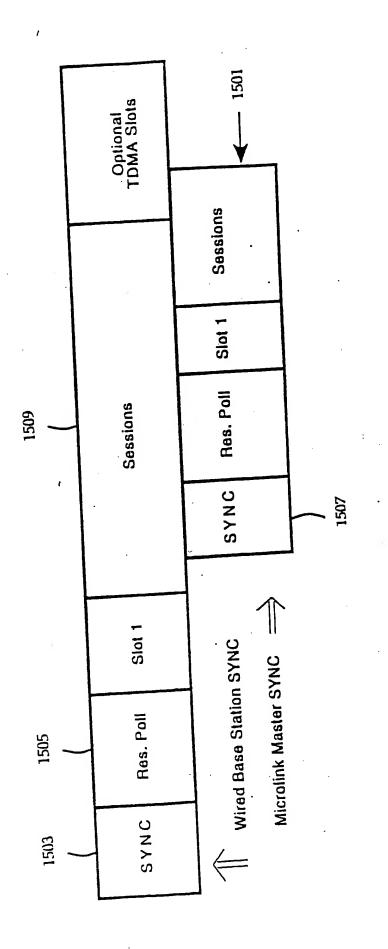


Figure 15

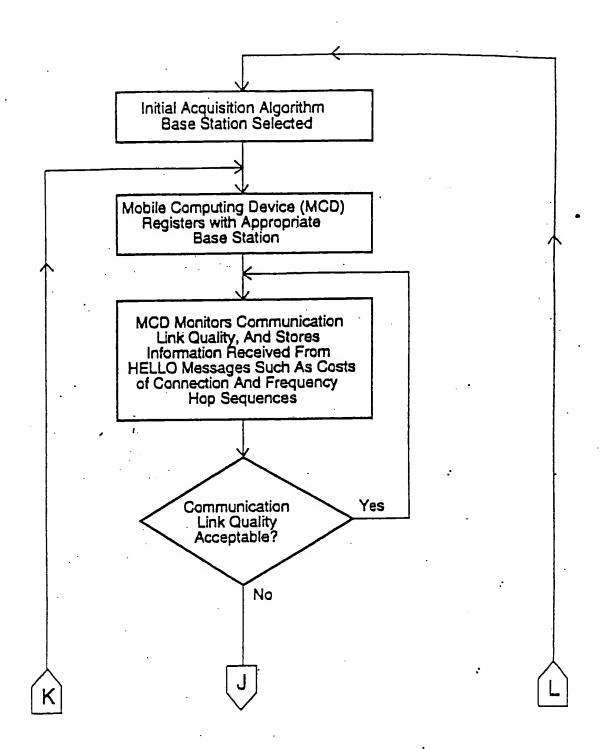
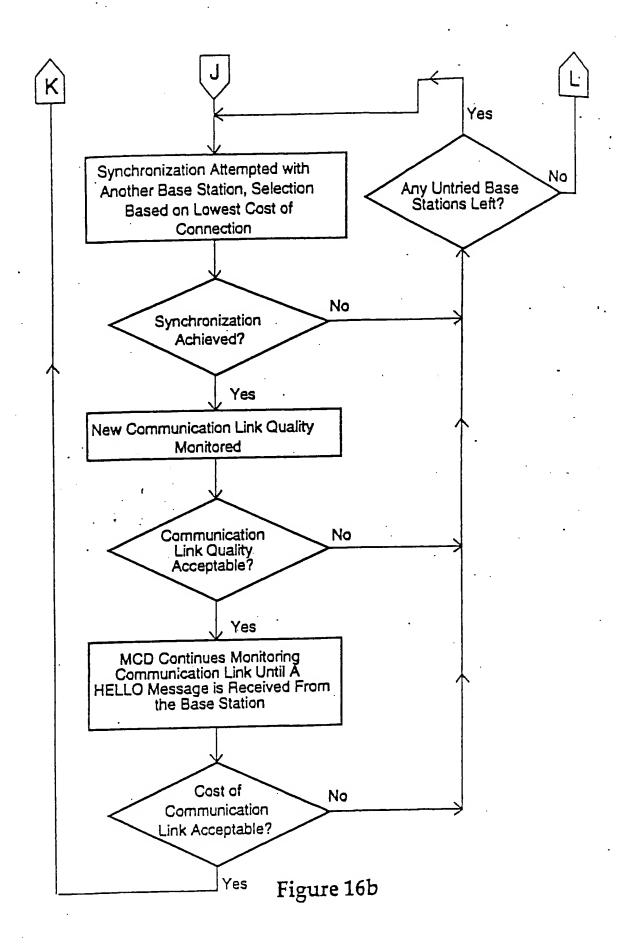


Figure 16a



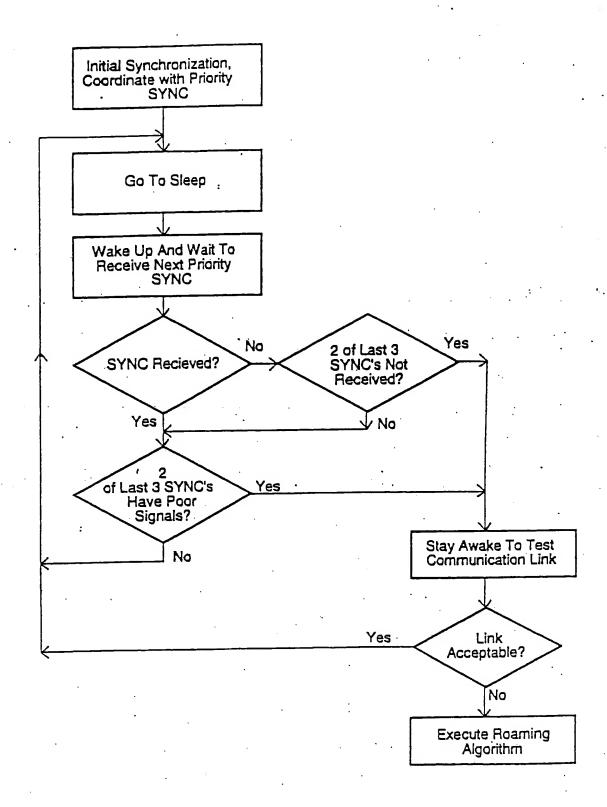
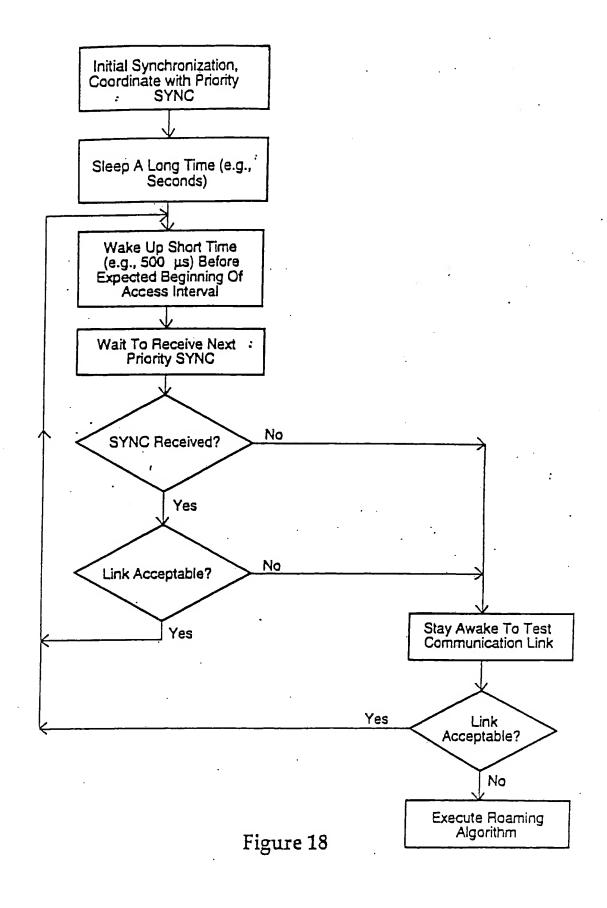
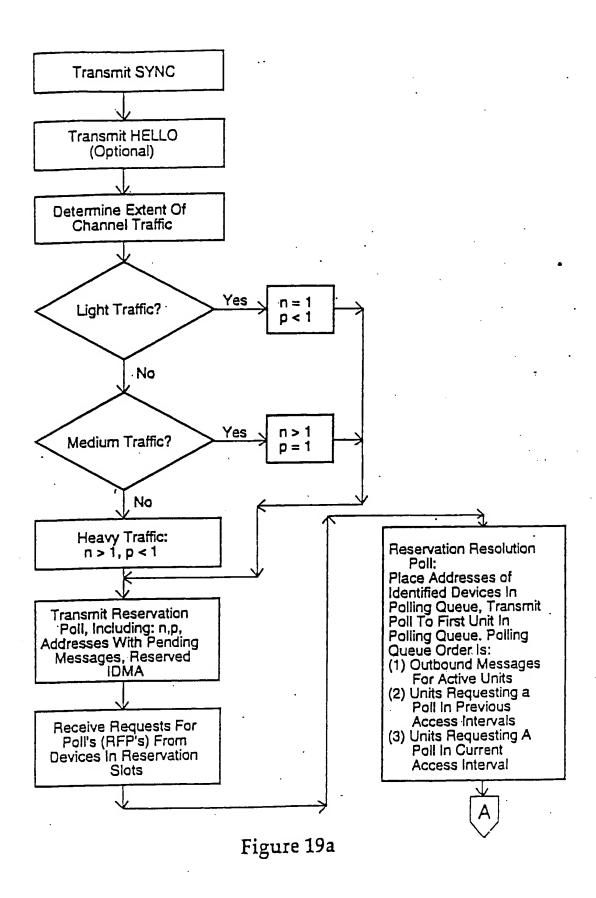


Figure 17





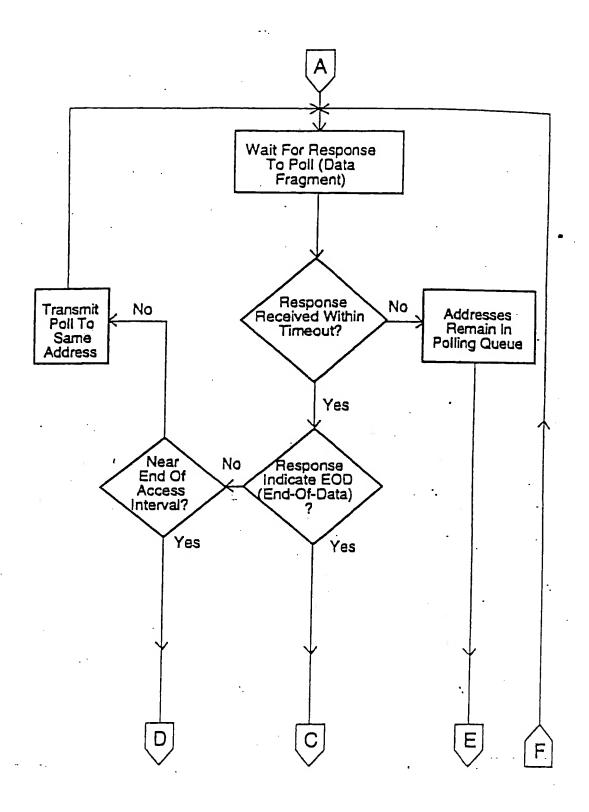


Figure 19b-1

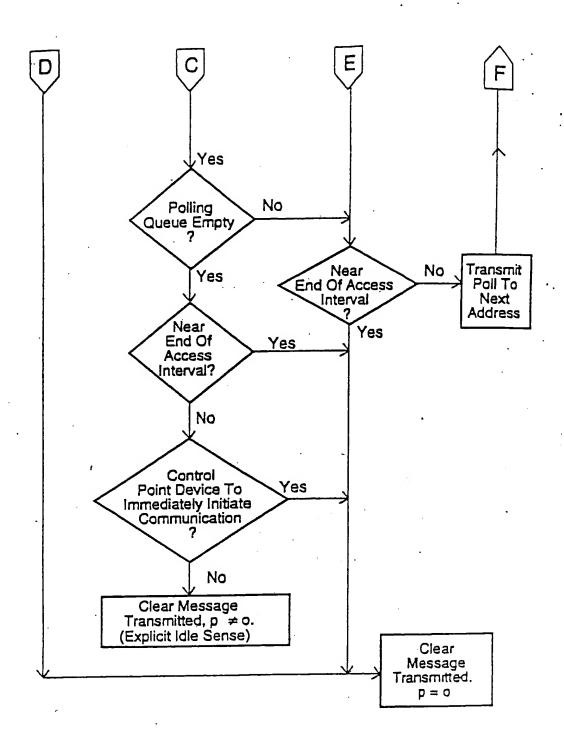


Figure 19b-2

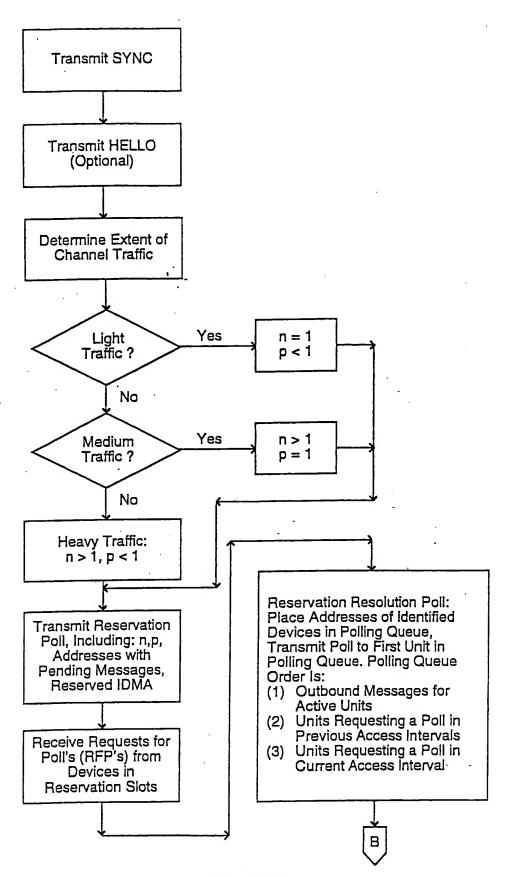


Fig. 20a

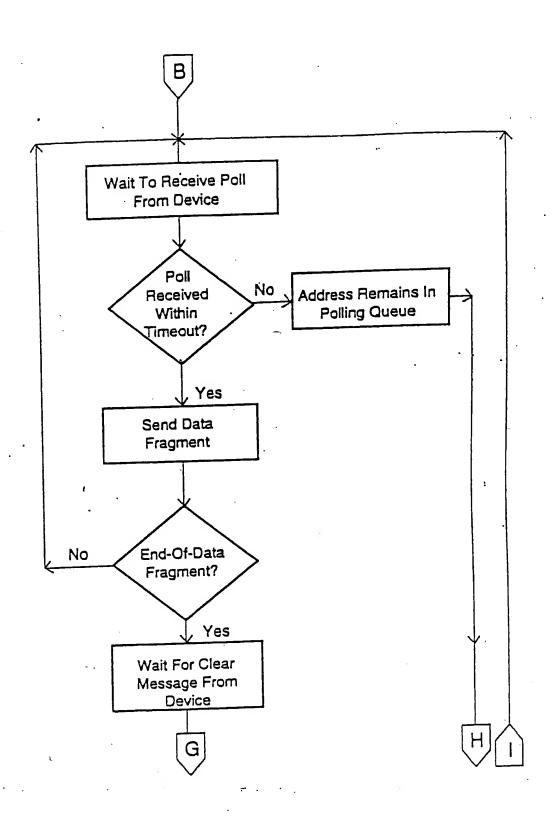


Figure 20b-1

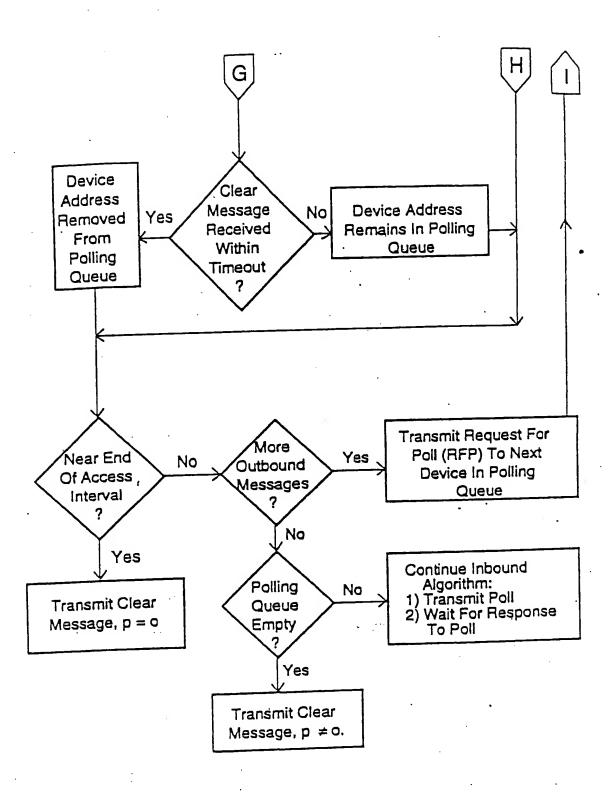
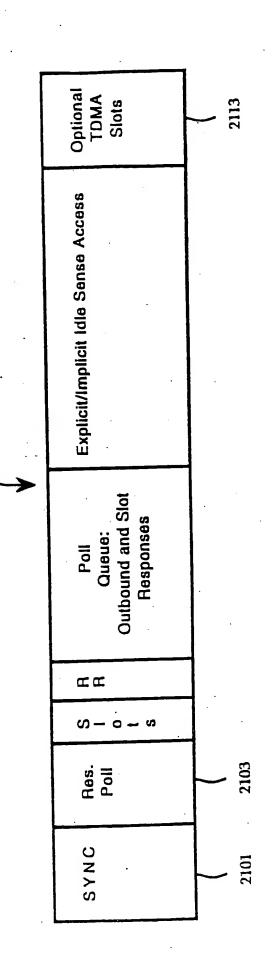


Figure 20b-2



2100

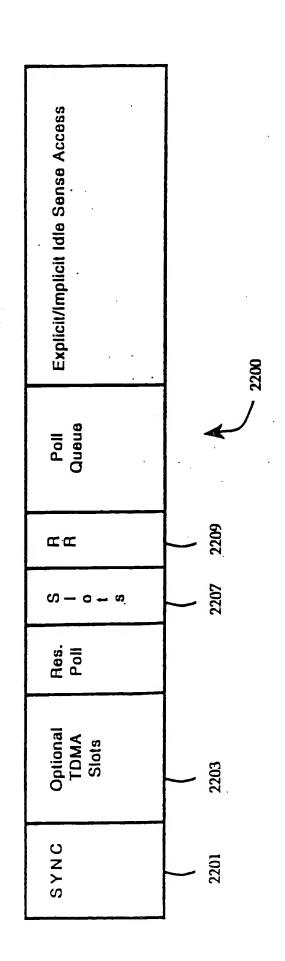


Figure 22

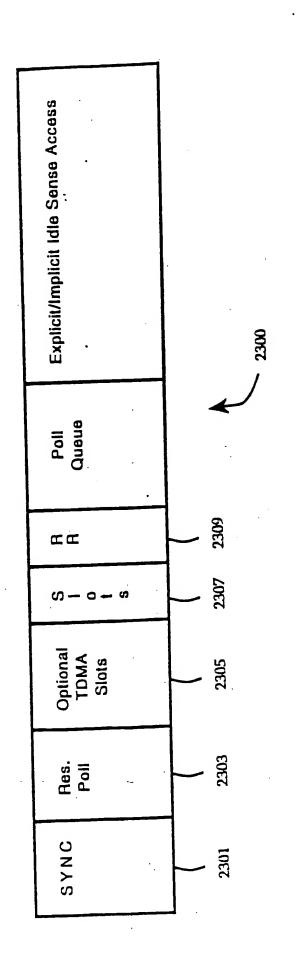


Figure 23

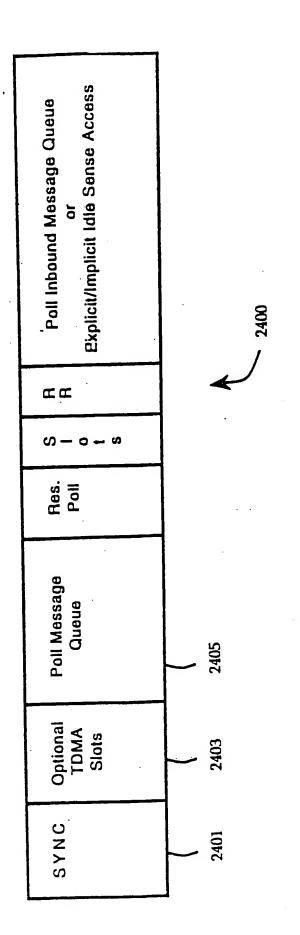
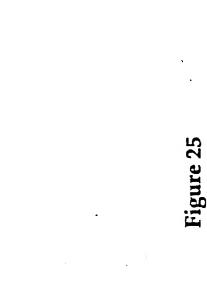


Figure 24



110 µs ++ Res. Poll 12 bytes 2505 120 µs ++ SYNC 14 bytes 2503 1 Training and Acquisition 2500 300 µsec 1 2501 ←150 µsec → Freq. Switching

++ allows additional bit times for HDLC zero bit insertion

		ON I S
Вуте	Content	Definition
-	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with
		HELLO
5	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8.	NET ID	Net ID
•6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10.	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Stots
		3 Bits Number of Available Time Slots
11"	Random	Randomly Selected Character for Improved Engages
12	CRC 1	Fror Chack
13	CAC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26

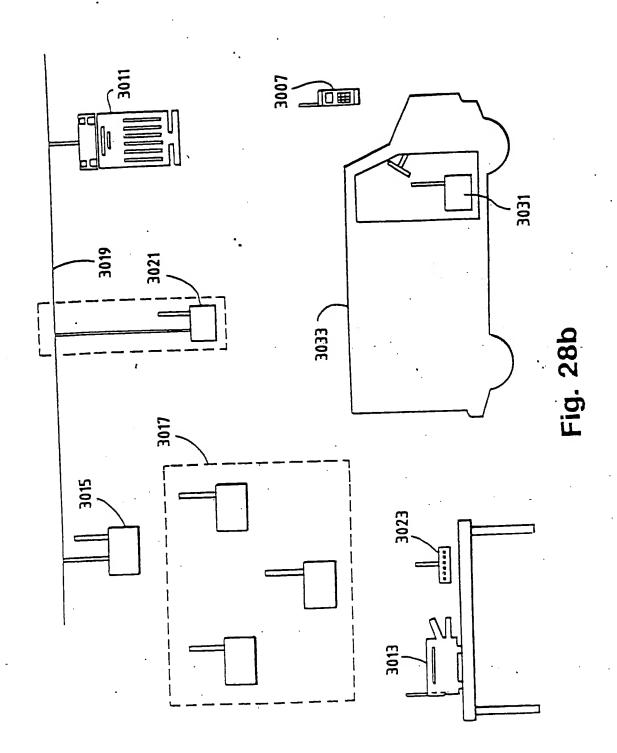
*Optionally Encrypted Bytes

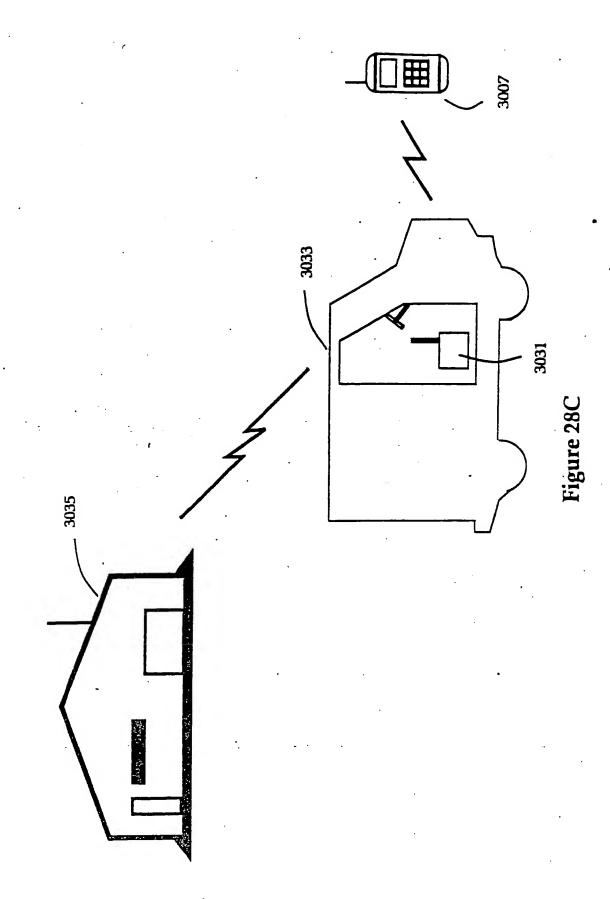
Reservation Poll

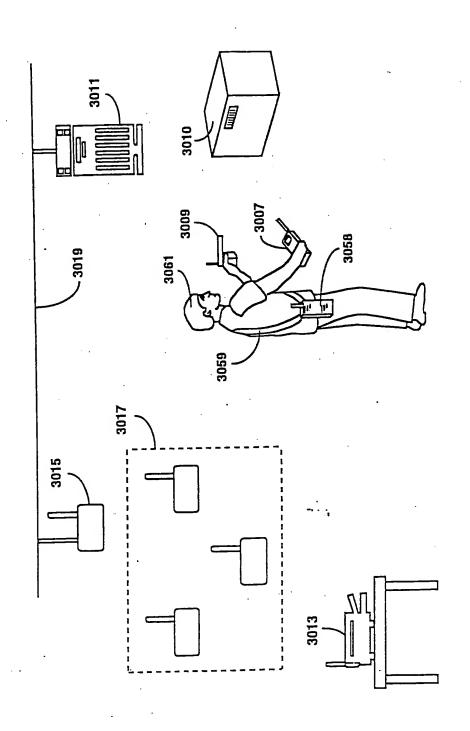
Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFF_H)	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Check	. Error Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS		Sequence	, Index	NET PARMS				CRC 1	CAC 2	FLAG
Byte	1	2	3	4	5	9	7*	8•	. 6				10	1.1	12

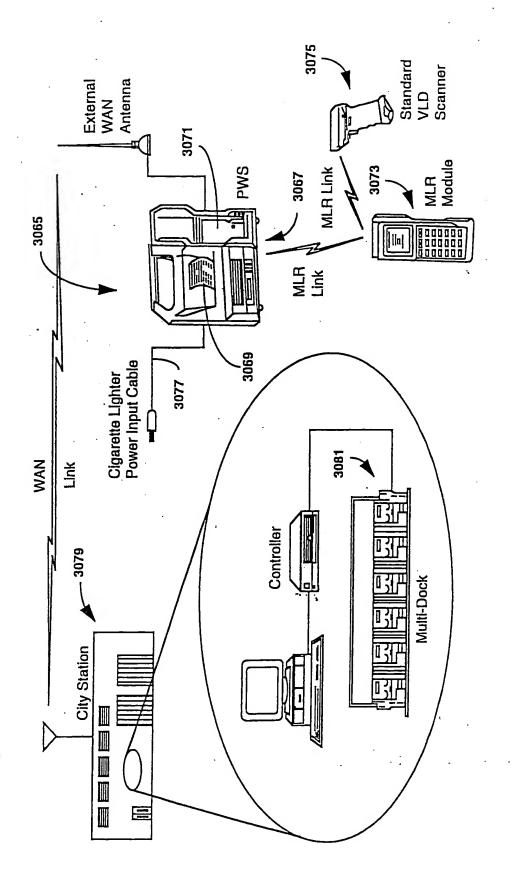
Figure 27

Fig. 28A









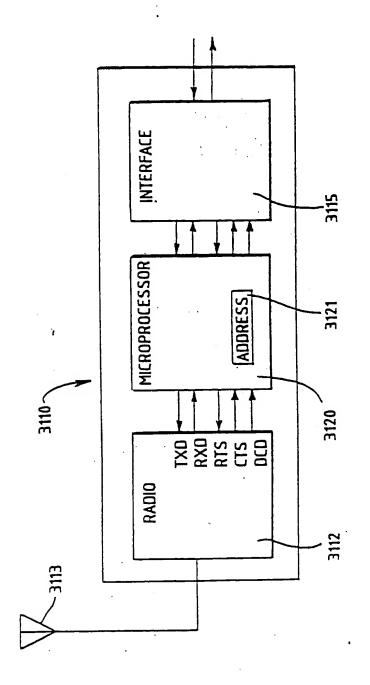


Fig. 30

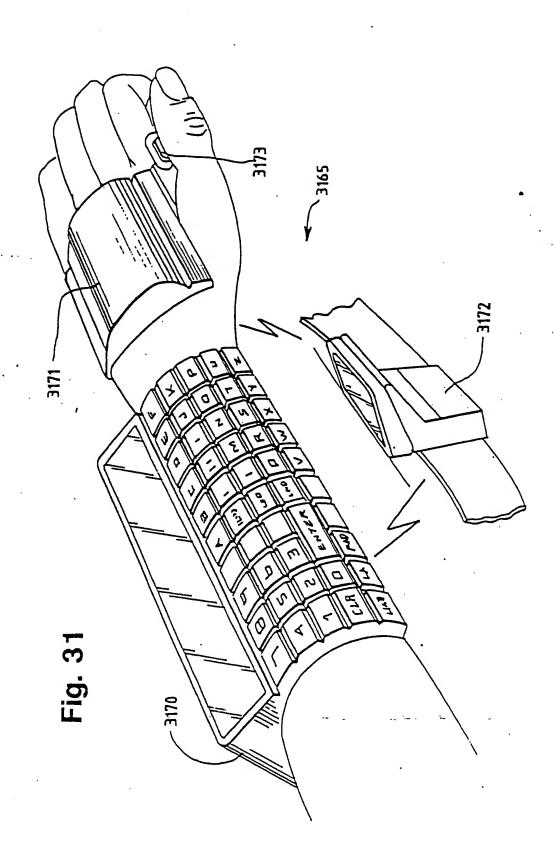
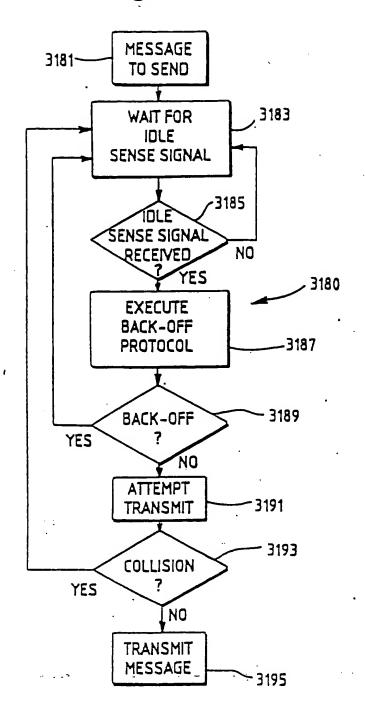


Fig. 32



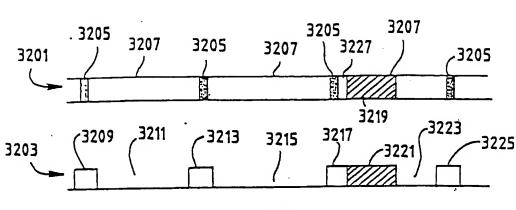
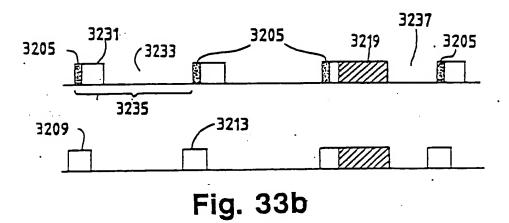
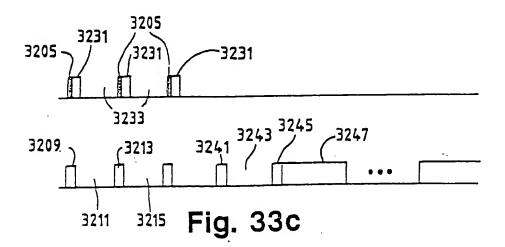


Fig. 33a





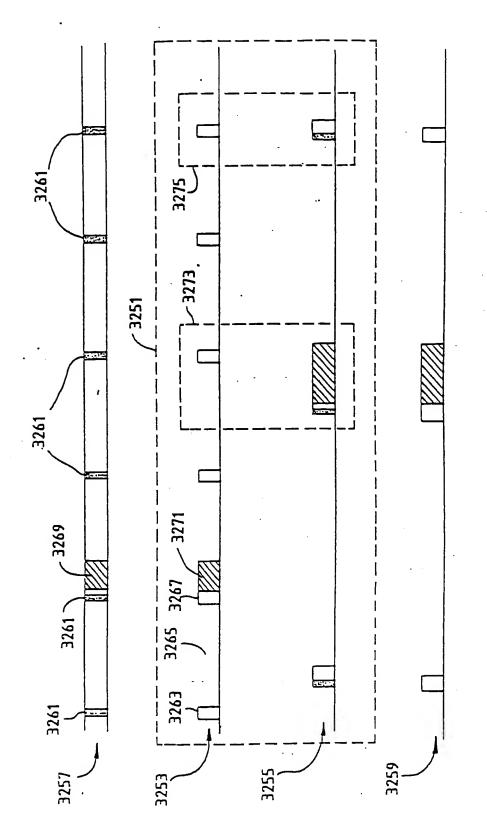


Fig. 34

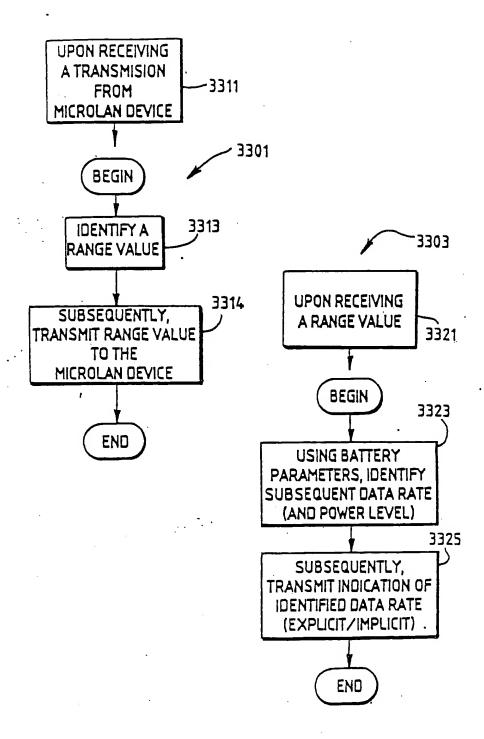
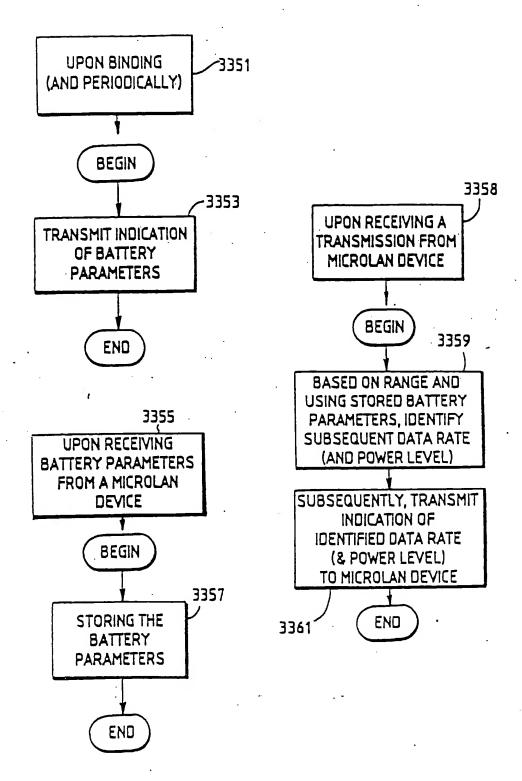


Fig. 35

Fig. 36



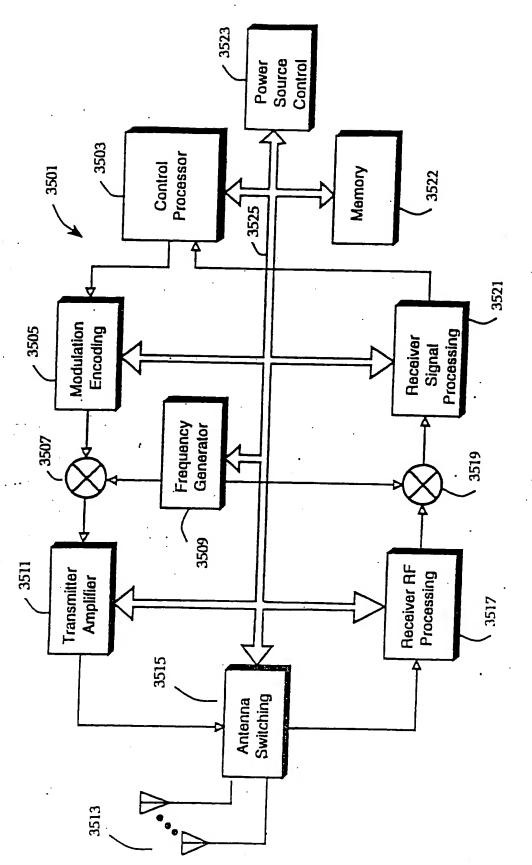


Figure 37

Figure 38

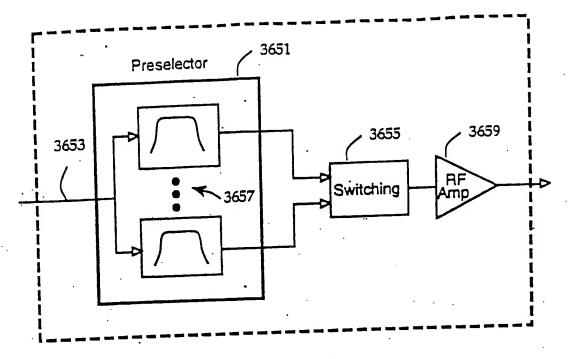


Figure 39

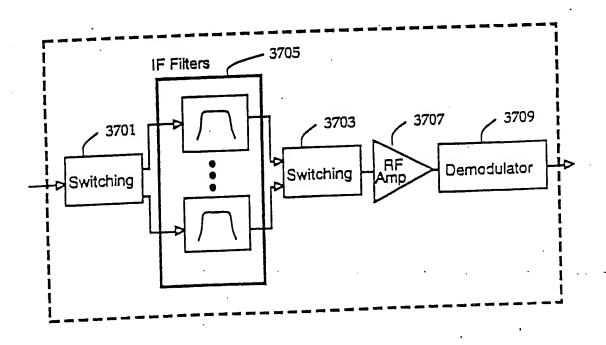


Figure 40

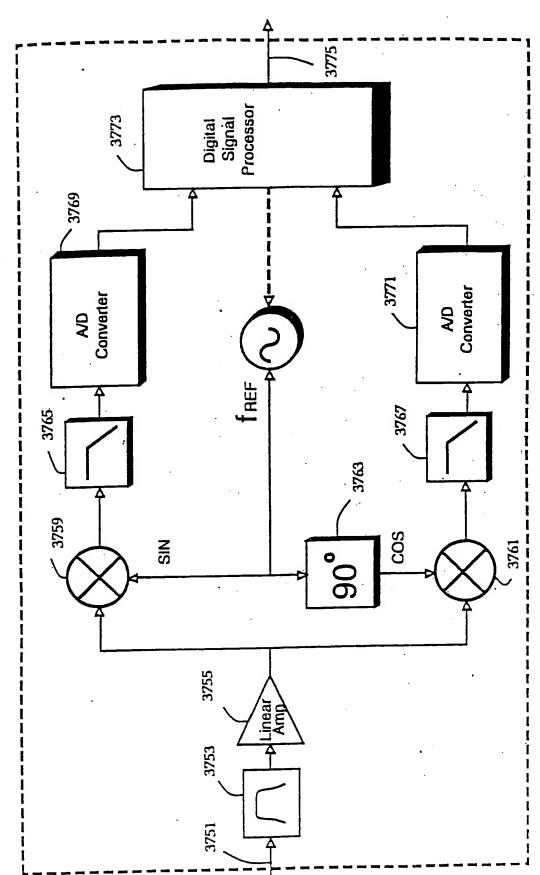


Figure 41

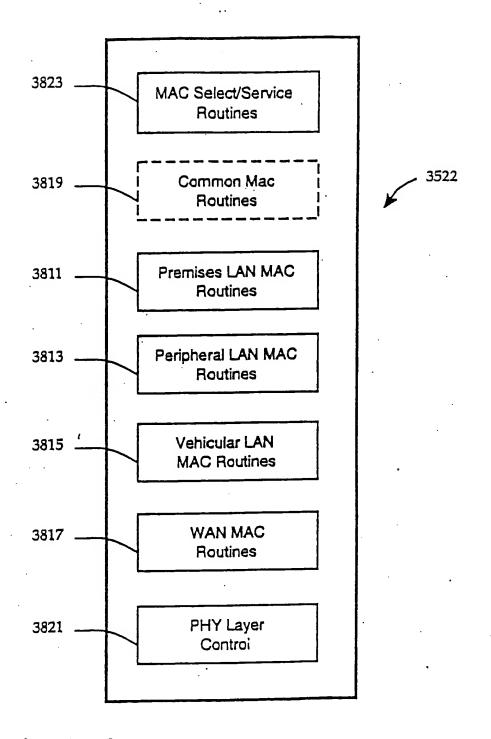


Figure 42

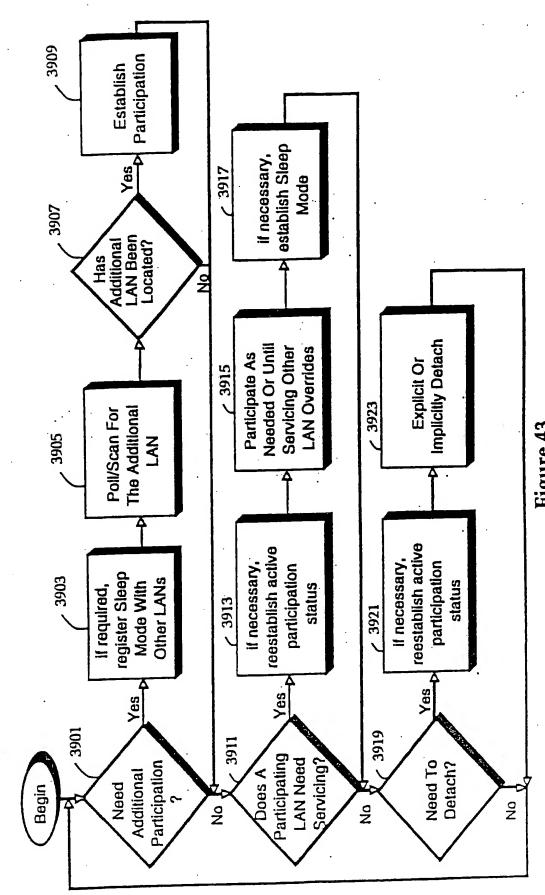


Figure 43

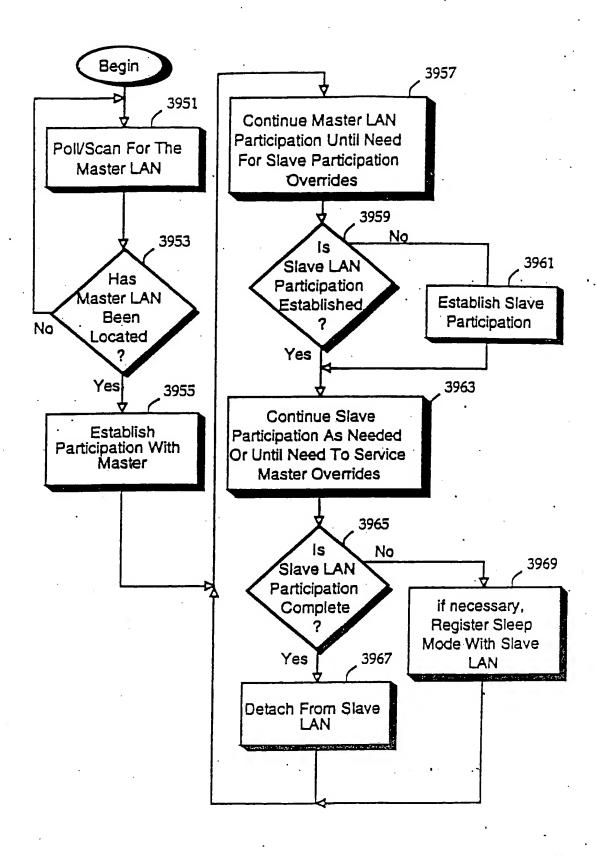


Figure 44

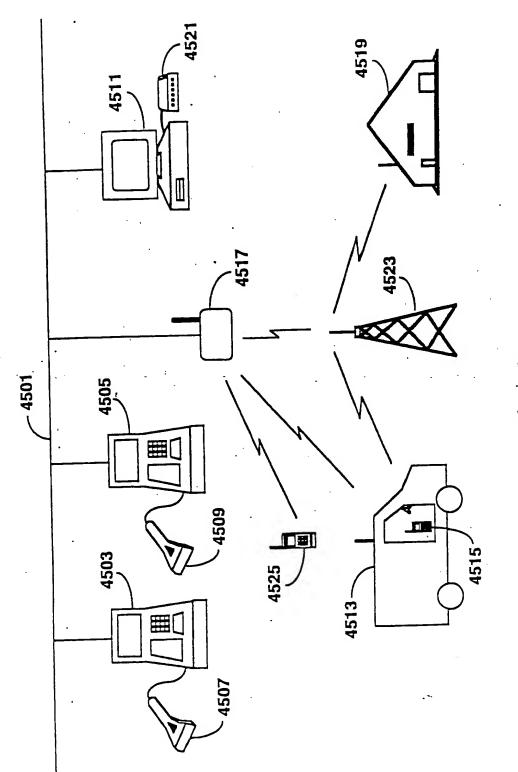


Fig. 45

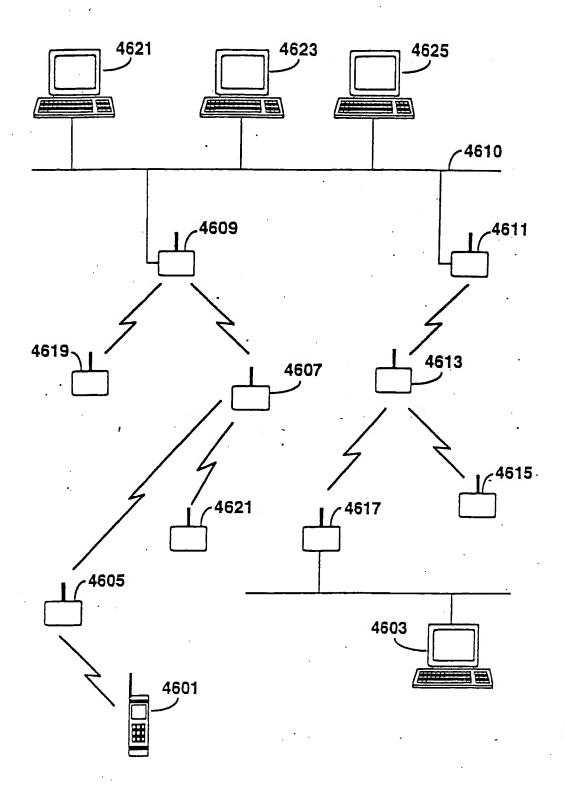


Fig. 46a

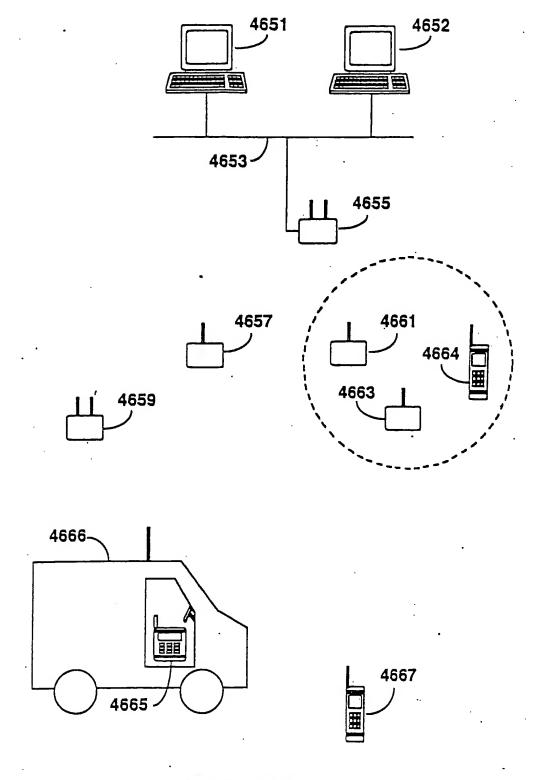


Fig. 46b

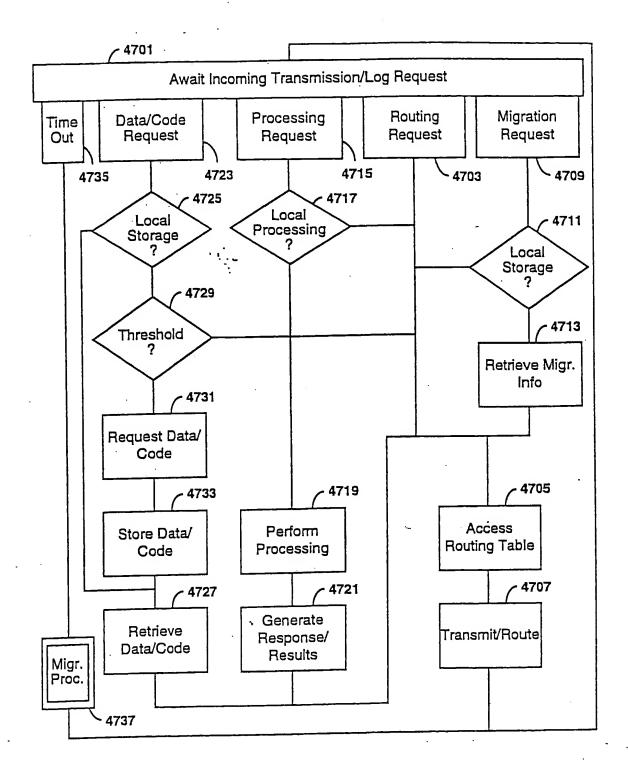


Fig. 47a

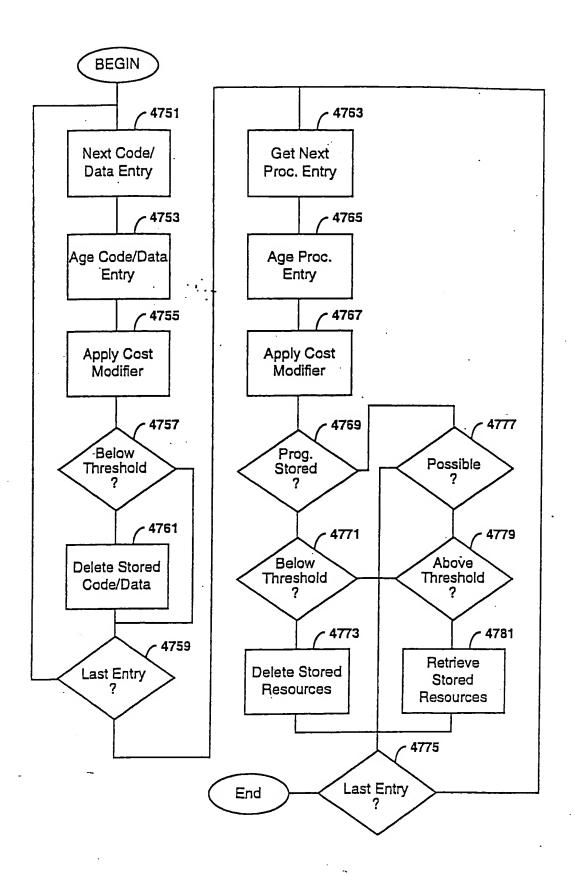


Fig. 47b

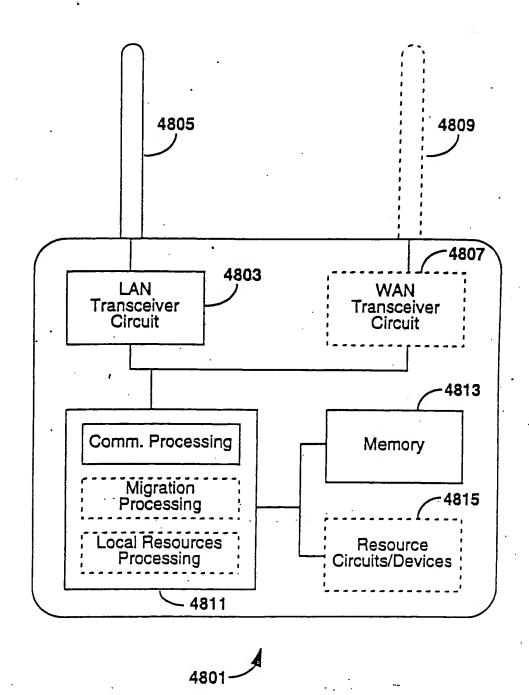


Fig. 48

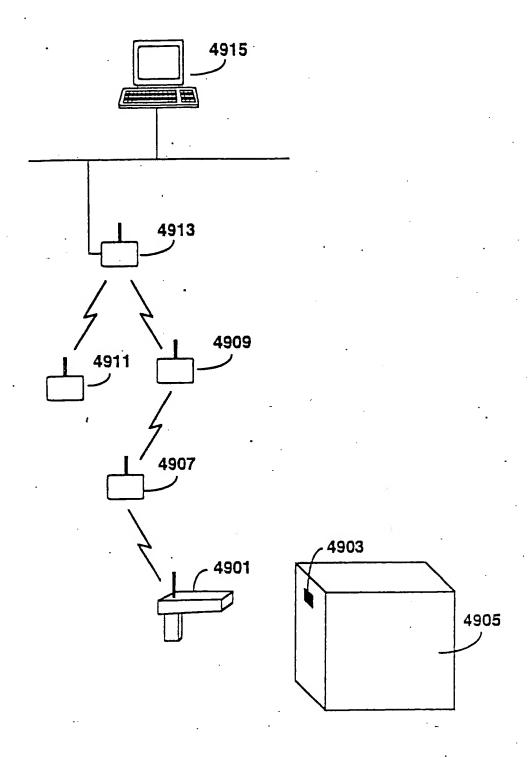


Fig. 49a

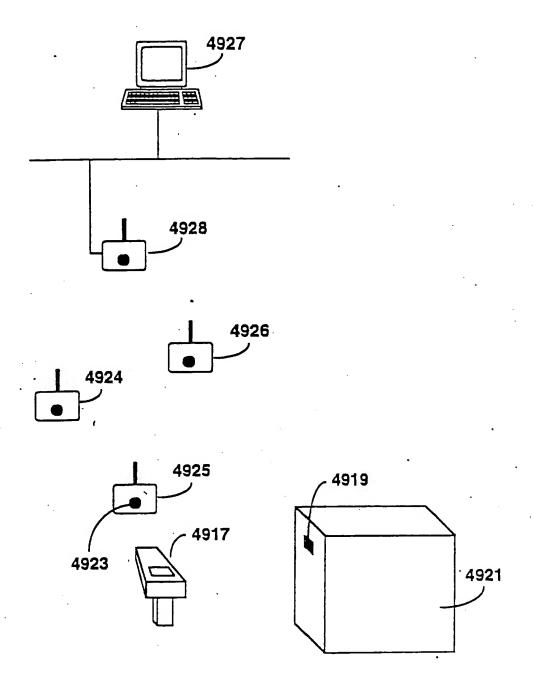


Fig. 49b

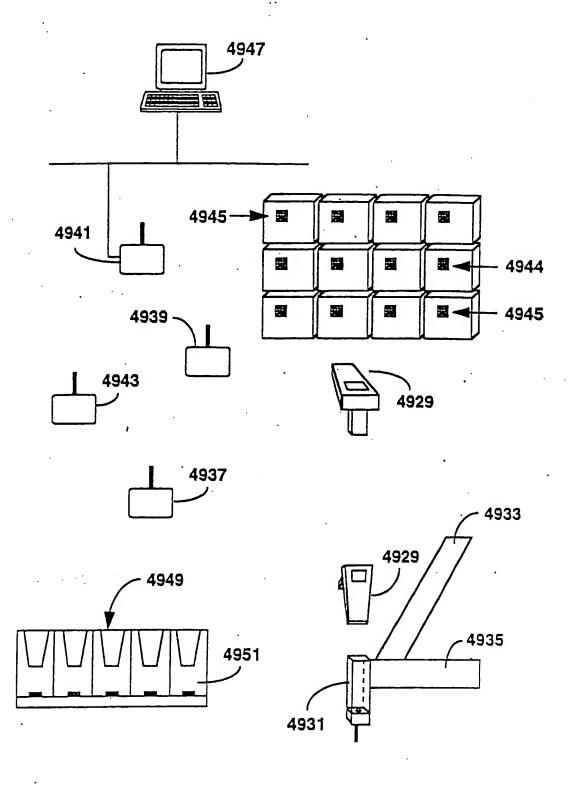


Fig. 49c

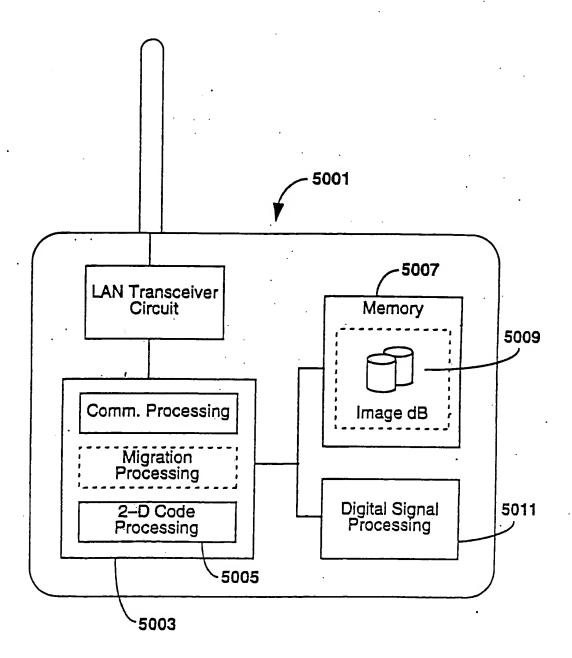


Fig. 50

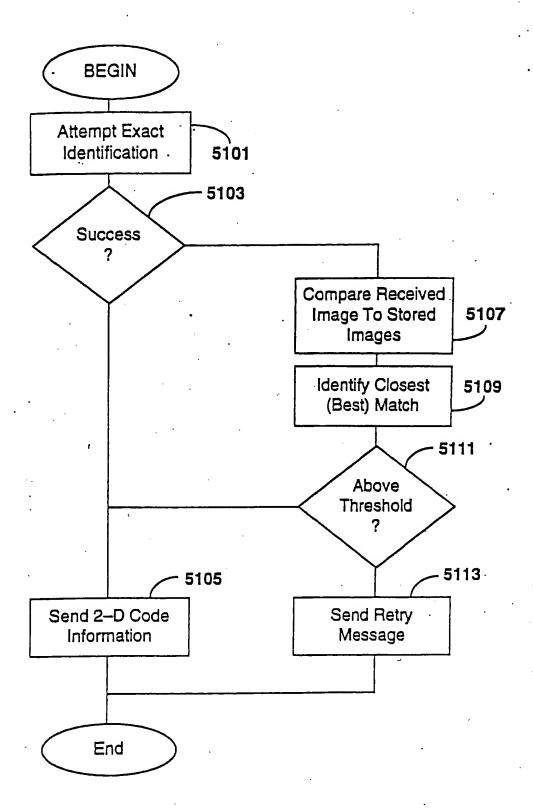


Fig. 51a

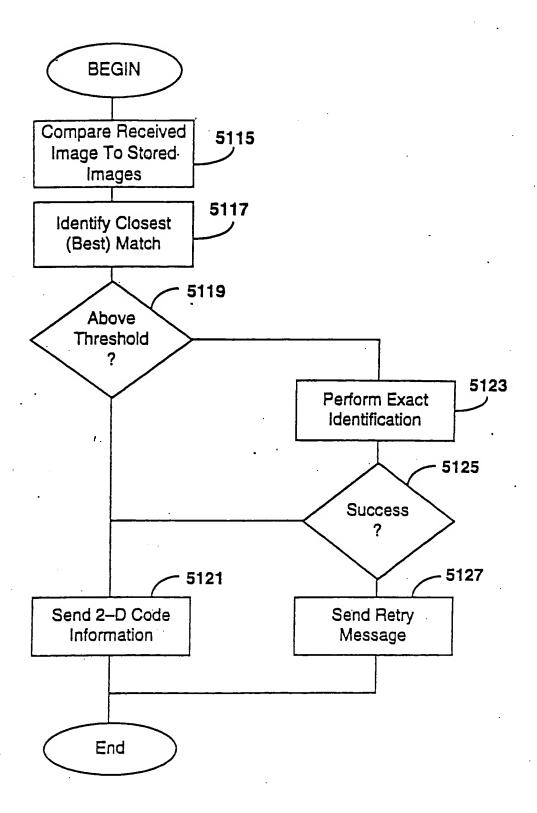
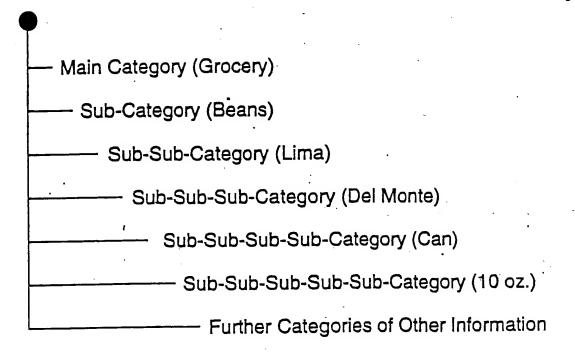


Fig. 51b



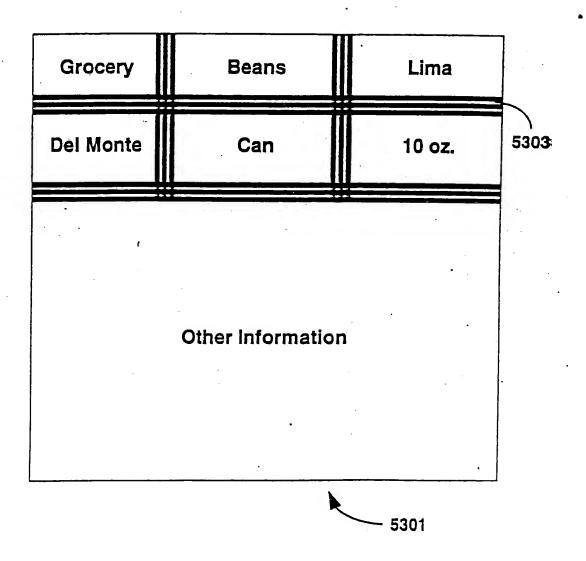


Fig. 53

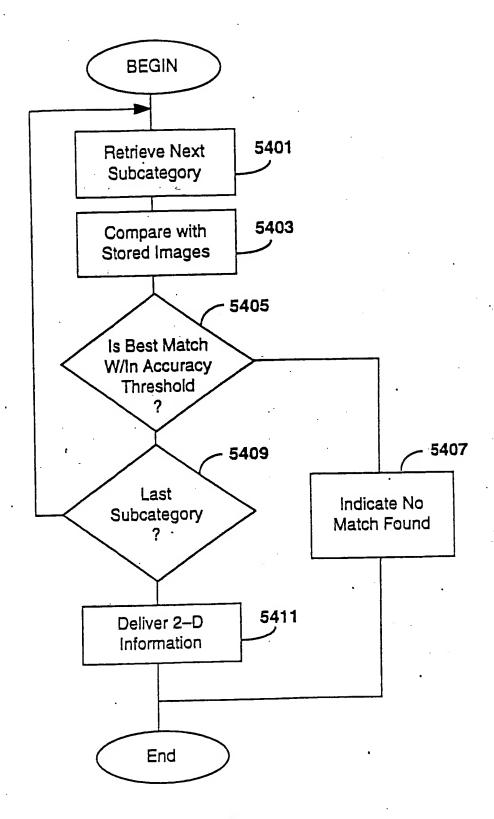


Fig. 54

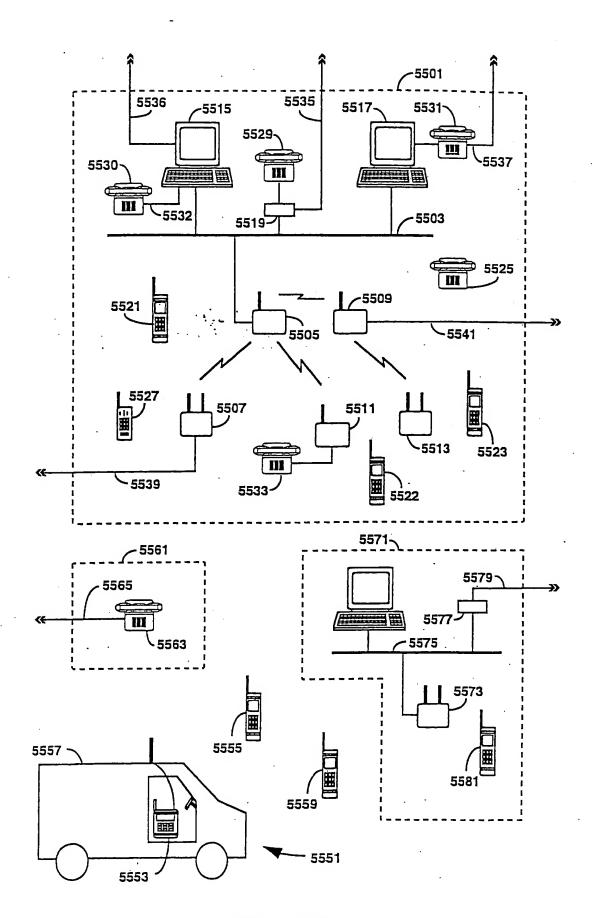


Fig. 55a

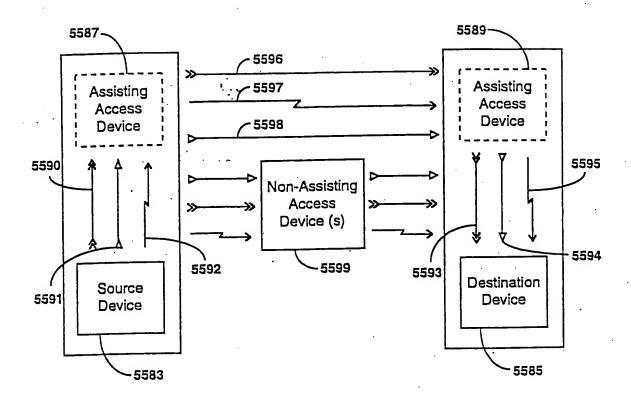


Fig. 55b

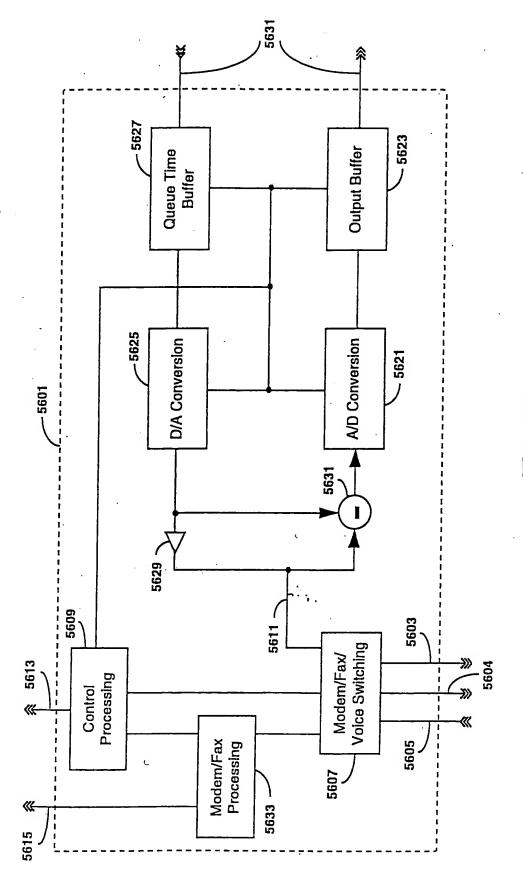


Fig. 56a

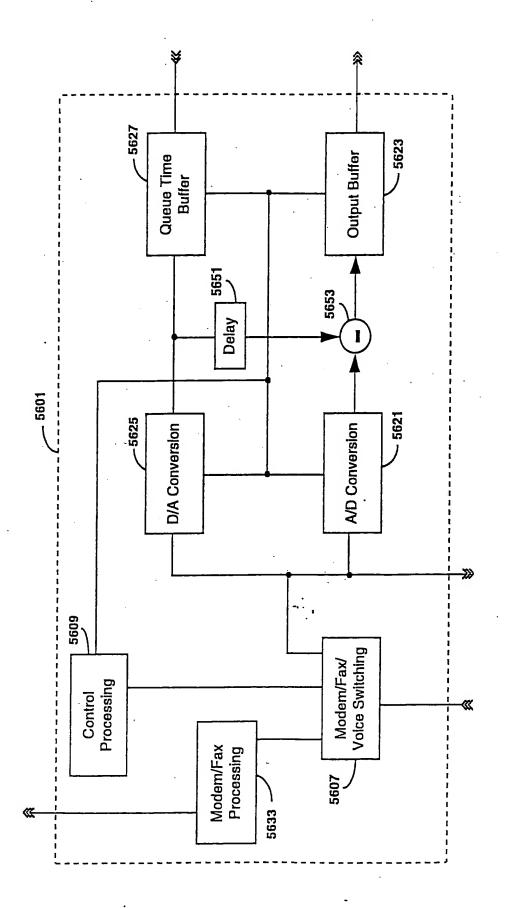


Fig. 56b

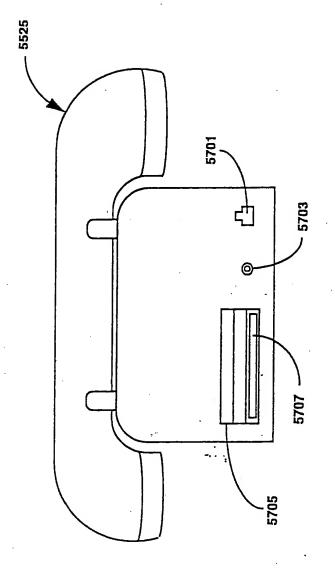


Fig. 57

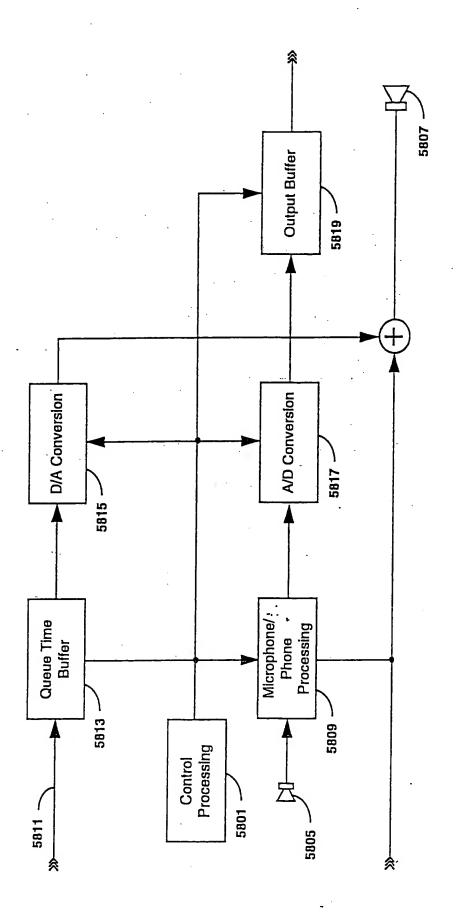


Fig. 58

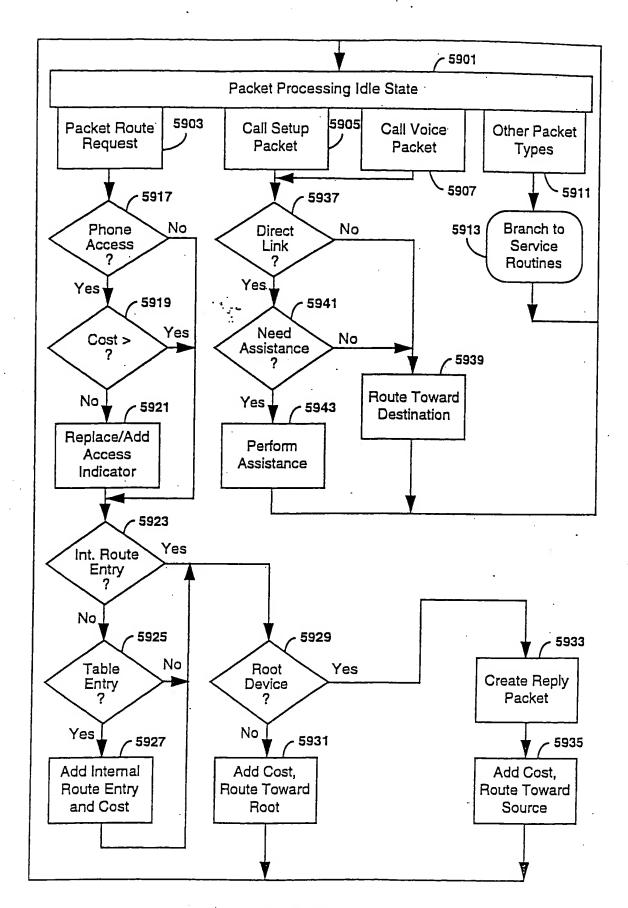


Fig. 59

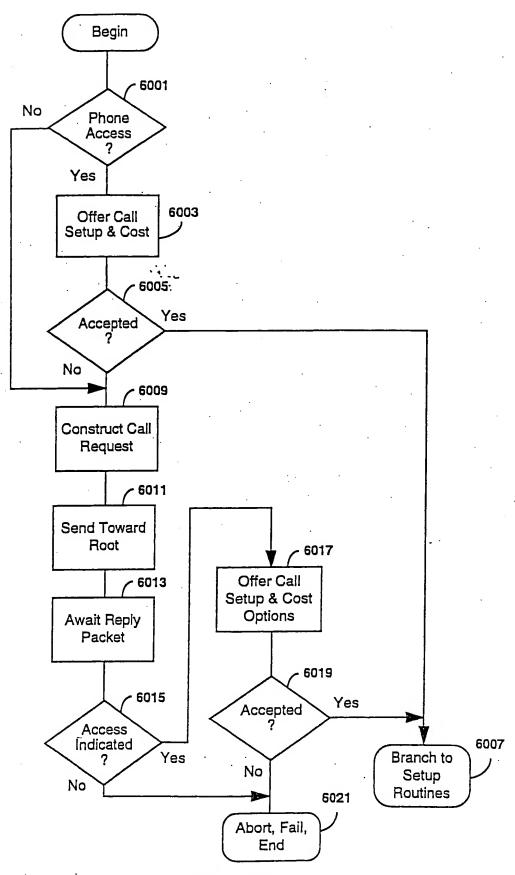


Fig. 60

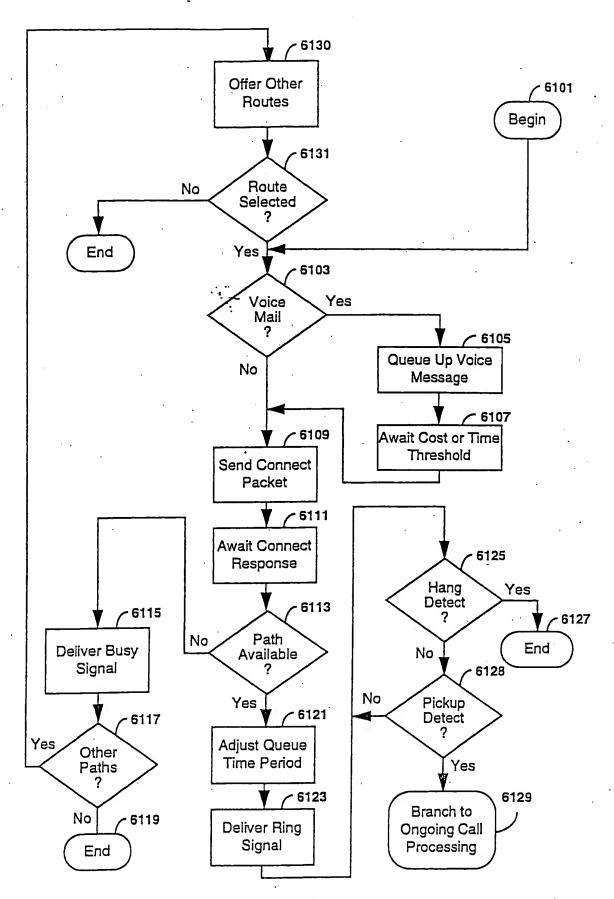


Fig. 61

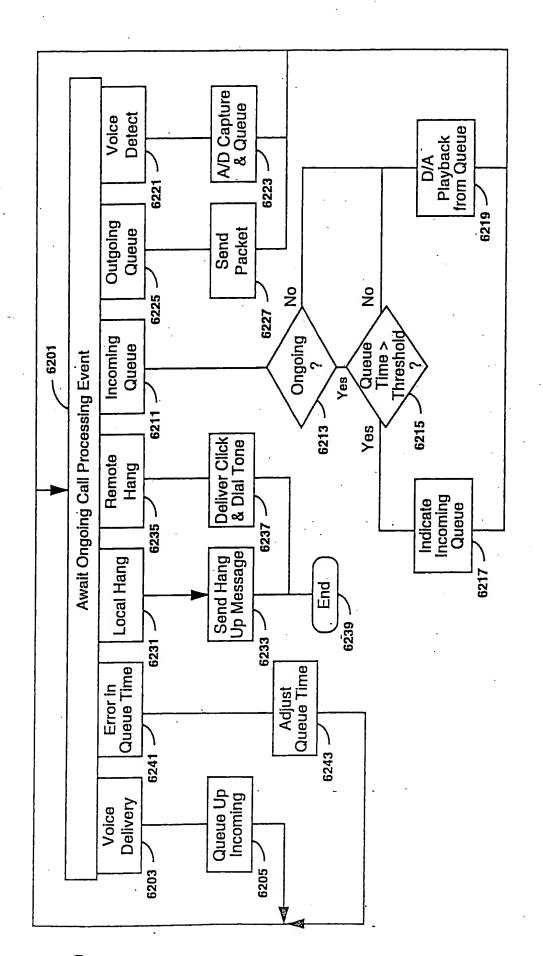


Fig. 62

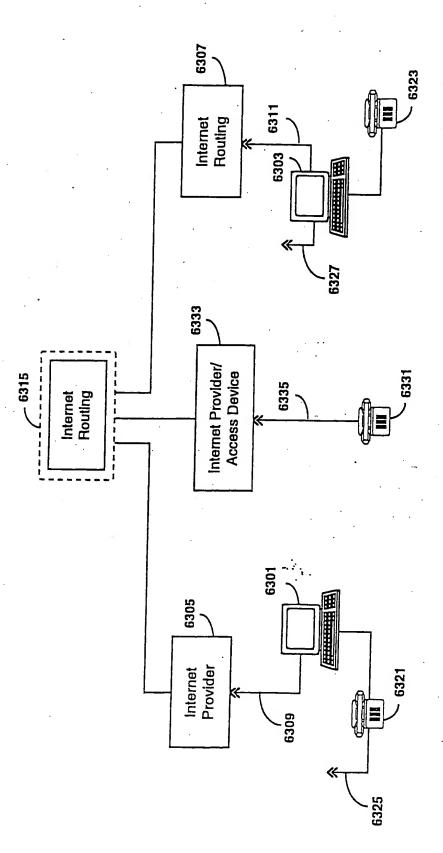


Fig. 63